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Attorney Docket No.  
033035 M 084

**RESPONSE UNDER 37 C.F.R. § 1.116  
EXPEDITED PROCESSING  
GROUP ART UNIT: 2815**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:

**Seiji YAEGASHI, et al.**

Serial No. : 10/026,950

Filed : December 27, 2001

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Art Unit : 2815

Examiner : Paul E. Brock II

For : Heterojunction Bipolar Transistor and Method of Making  
Heterojunction Bipolar Transistor

**AFTER FINAL AMENDMENT  
UNDER 37 C.F.R. § 1.116**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

A response to the Office Action dated December 10, 2003 (Paper No. 14) is due by March 10, 2004. Please accept this Amendment as timely filed and fully responsive.

For the purpose of placing the above-captioned application in immediate condition for allowance, or alternatively, in better form for appeal, Applicants respectfully request entry and consideration of this Amendment.

In response to Paper No. 14, kindly amend the above-identified patent application as follows: